

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Earl Schreyer et al.
Title: Graphics Digitizer
Application No.: Unknown
Filing Date: Herewith
Examiner: Leland R. Jorgensen (in parent application)
Group Art Unit: 2675 (in parent application)
Confirmation No.: Unknown
Office: Sidley Austin Brown & Wood LLP
Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed are copies of the Disclosure Statements, PTO-1449 forms, and PTO-892 forms which were made of record in the parent application:

Application Serial No. 09/658,707, filed September 8, 2000.

The above-listed application is relied upon by the instant application being filed herewith for earlier filing dates under 35 U.S.C. Section 120.

Each document listed on the various statements are being incorporated herein as a Disclosure Statement submitted for the Examiner's consideration. The making of each of them of record in the file of this application, is respectfully requested.

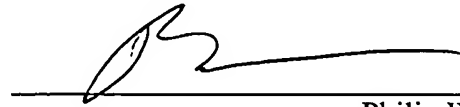
Since these references were previously filed by Applicants, or cited by the Examiner in the parent patent application Serial No. 09/658,707, no copies are included. Should copies be desired, the undersigned attorney would be glad to furnish them.

EXPRESS MAIL LABEL NO.:

Respectfully submitted,

EV 305 258 466 US

By:

A handwritten signature in black ink, appearing to be 'Philip W. Woo', is written over a horizontal line.

Philip W. Woo
Attorney of Record
Registration No. 39,880
PWW/rp

March 23, 2004
SIDLEY AUSTIN BROWN & WOOD LLP
555 California Street, Suite 5000
San Francisco, CA 94104-1715

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Earl Schreyer, Ken Martin, David Johns, Raymond Chik and
Doug Moran
Title: GRAPHICS DIGITIZER
Serial No.: 09/658,707 Filed: September 8, 2000
Examiner: Unknown Group Art Unit: 2671
Docket No.: M-9327 US

San Jose, California
July 23, 2002

COMMISSIONER FOR PATENTS
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants bring the following documents (copies enclosed) listed on the enclosed PTO form 1449 to the Examiner's attention in the above-captioned application.

Citation of the above documents shall not be construed as:


1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account 19-2386.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on July 23, 2002.


Attorney for Applicants
7/23/02
Date of Signature

Respectfully submitted,


Gary J. Edwards
Attorney for Applicants
Reg. No. 41,008

U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Serial No.	
					M-9327 US		09/658,707	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicants			
(Use several sheets if necessary)					Earl Schreyer et al.			
					Filing Date		Group	
					September 8, 2000		2671	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	5,039,954	Aug. 13, 1991	Bult et al.	330	277		
	AB	5,285,171	Feb. 8, 1994	Bult et al.	330	277		
	AC	5,475,331	Dec. 12, 1995	Bult et al.	327	427		
	AD	5,677,621	Oct. 14, 1997	Bult et al.	323	315		
	AE	5,835,048	Nov. 10, 1998	Bult	341	159		
	AF	6,045,710	Apr. 4, 2000	Silverbrook	216	2		
	AG	6,011,538	Jan. 4, 2000	Eglit	345	127		
	AH	6,014,098	Jan. 11, 2000	Bult et al.	341	158		
	AI	6,100,836	Aug. 8, 2000	Bult	341	155		
	AJ							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AK							
	AL							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AM	"FMS9884A Graphics Digitizer 3x8-Bit, 100/140 Ms/s Triple Video A/D Converter with Clamps" Fairchild Semiconductor (2000), 30 pages.						
	AN	"Computer RGB colour space", downloaded 08/08/2000 from < http://www.robo.mein.nagoya-u.ac.jp/~niimi/color-space/COL_21.htm >						
	AO	"Technotes Color Monitor Connections", downloaded 08/08/2000 from < http://devworld.apple.com/technotes/hw/hw_08.html >, Apple Computer, Inc. (2000).						
	AP	"The PC Guide", downloaded 08/08/2000 from < http://www.pcguide.com/ref/crt >, The PC Guide (1997-2000), 19 pages.						
Examiner			Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								

Notice of References Cited	Application/Control No. 09/658,707	Applicant(s)/Patent Under Reexamination SCHREYER ET AL.	
	Examiner Leland R. Jorgensen	Art Unit 2675	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,175,323 B1	01-2001	Flynn	341/156
	B	US-6,333,750 B1	12-2001	Odryna et al.	345/629
	C	US-6,326,911 B1	12-2001	Gomez et al.	341/143
	D	US-6,278,395 B1	08-2001	Ito et al.	341/166
	E	US-6,232,907 B1	05-2001	Nagaraj et al.	341/159
	F	US-6,069,579 B1	05-2000	Ito et al.	341/156
	G	US-6,255,971 B1	04-2001	Van de Plassche et al.	341/120
	H	US-5,835,047	11-1998	Varenkamp et al.	341/156
	I	US-5,126,742	08-1992	Schmidt et al.	341/156
	J	US-4,912,469	03-1990	Van De Grift et al.	341/159
	K	US-4,325,054	04-1982	van de Plassche	341/899
	L	US-5,117,442	05-1992	Hall	375/356
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP 409163340 A	08-1997	Japan	Uemura et al.	H04N 007/167
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Rob E. J. Van De Grift, et al. "An 8-bit Video ADC Incorporating Folding and Interpolation Techniques", IEEE Journal of Solid-State Circuits, vol. sc-22. No. 6. Dec. 1987. pp. 944-953
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Earl Schreyer et al.
Title: Graphics Digitizer
Application No.: 09/658,707
Filing Date: September 8, 2000
Examiner: Leland R. Jorgensen
Group Art Unit:: 2675
Confirmation No.: 3672
Law Office: Sidley Austin Brown & Wood LLP
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicant wishes to call the following documents (copies enclosed) to the attention of the Examiner for the above-identified patent application.

A form PTO-1449 listing these documents is enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or

3. an admission that the information listed herein is, or is considered to be, material to patentability as defined in § 1.56(b).

Please charge Deposit Account No. 50-1597 in the amount of \$180.00 for the Information Disclosure Statement fee under 37 C.F.R. § 1.17(p), as set forth in the accompanying transmittal letter. The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 50-1597.

EXPRESS MAIL LABEL NO.:

EV 305 256 536 US

Respectfully submitted,

By:



Philip W. Woo
Attorney of Record
Reg. No. 39,880
PWW/rp

July 9, 2003
SIDLEY AUSTIN BROWN & WOOD LLP
555 California Street, Suite 5000
San Francisco, CA 94104-1715

U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.		Application No.	
					24317/81001		09/658,707	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicants			
(Use several sheets if necessary)					Earl Schreyer et al.			
					Filing Date		Group	
					09/08/2000		2675	
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	1.	6,147,668	Nov. 14, 2000	Eglit				
	2.	6,307,498 B1	Oct. 23, 2001	Eglit				
	3.	6,320,574 B1	Nov. 20, 2001	Eglit				
	4.	6,459,426 B1	Oct. 1, 2002	Eglit et al.				
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
Examiner Signature:					Date Considered:			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								

Express Mail Label No.: EV 305 256 536 US

Notice of References Cited	Application/Control No. 09/658,707	Applicant(s)/Patent Under Reexamination SCHREYER ET AL.	
	Examiner Leland R. Jorgensen	Art Unit 2675	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,204,794	11-1998	Bult	341/155
	B	US-5,751,236	05-1998	Vorenkamp et al.	341/155
	C	US-4,831,379	05-1989	van de Plassche	341/156
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Michael Flynn et al., CMOS Folding ADCs with Current-Mode Interpolation, IEEE International Solid-State Circuits Conference Feb. 17, 1995, pp. 274 - 276.
	V	Michael Flynn et al., CMOS Folding A/D Converters with Current-Mode Interpolation, IEEE Journal of Solid-State Circuits, Vol. 31, No. 9, September 1996, pp. 1248 - 1257.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.